Central Processor

Base Station: TR/DH Receiver & TDOA Processor

Base Station: TR/DH Receiver & TDOA Processor

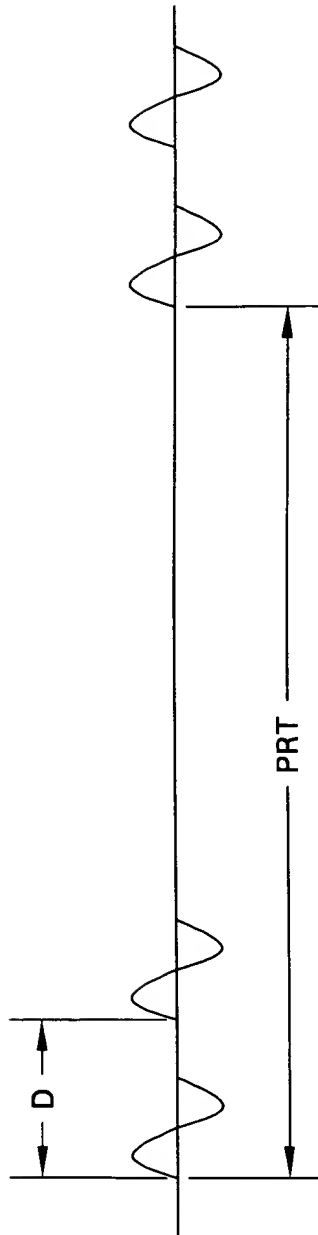Base Station: TR/DH Receiver & TDOA Processor
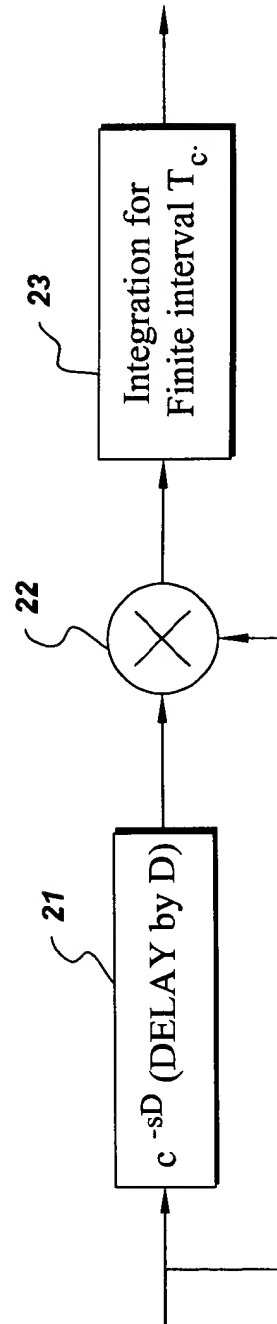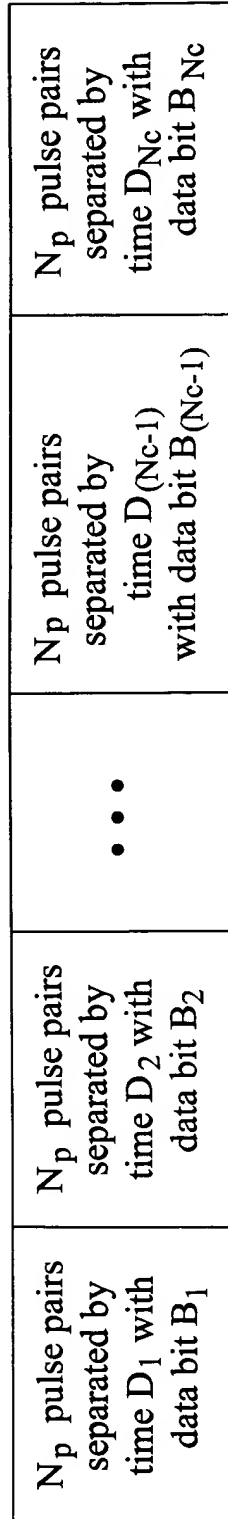
Mobile Device

Beacon Device

Fig. 1

Fig. 2

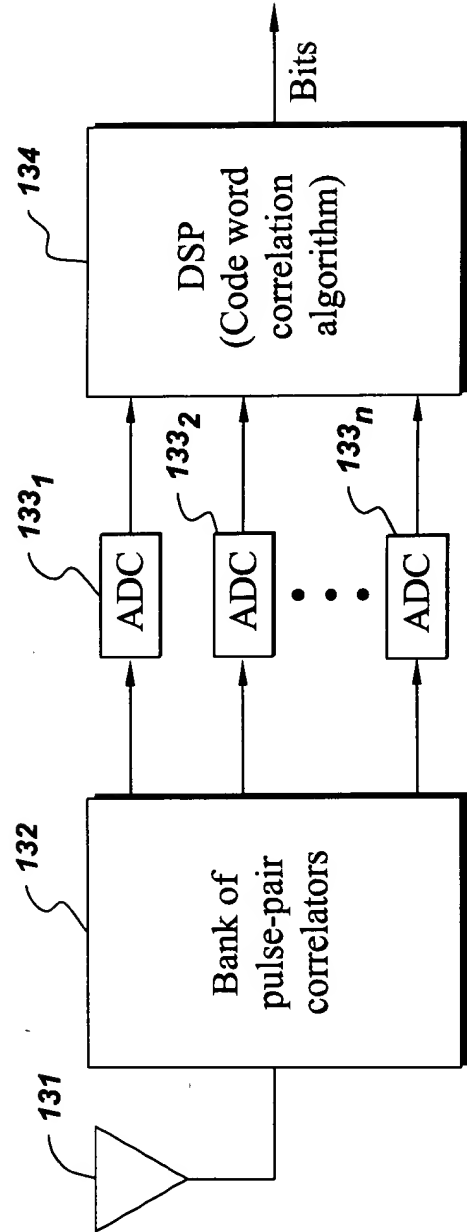*Fig. 3*

er.No.: 09/903,140
Inventor(s): Hoctor et al.
Docket No.: RD-27,855
Attorney: J.M. Breedlove
Telephone: (518) 387-6276

3/5

N_p pulse pairs separated by time D_1 with data bit B_1	N_p pulse pairs separated by time D_2 with data bit B_2	$\cdots$	N_p pulse pairs separated by time $D_{(Nc-1)}$ with data bit $B_{(Nc-1)}$	N_p pulse pairs separated by time D_{Nc} with data bit B_{Nc}

Fig. 4

Fig. 5

Fig. 6

Fig. 7

er.No.: 03/973,140
Inventor(s): Noctor et al.
Docket No.: RD-27,855
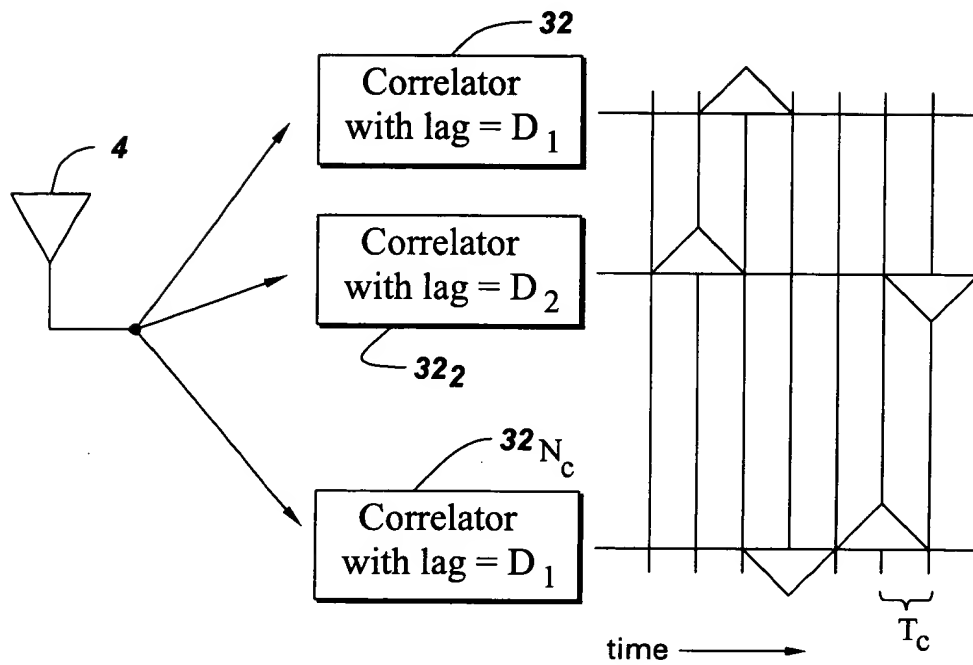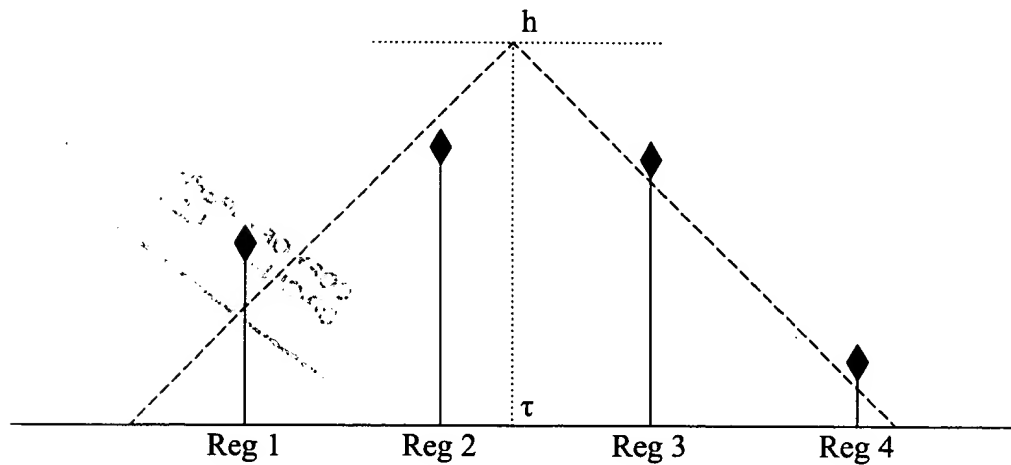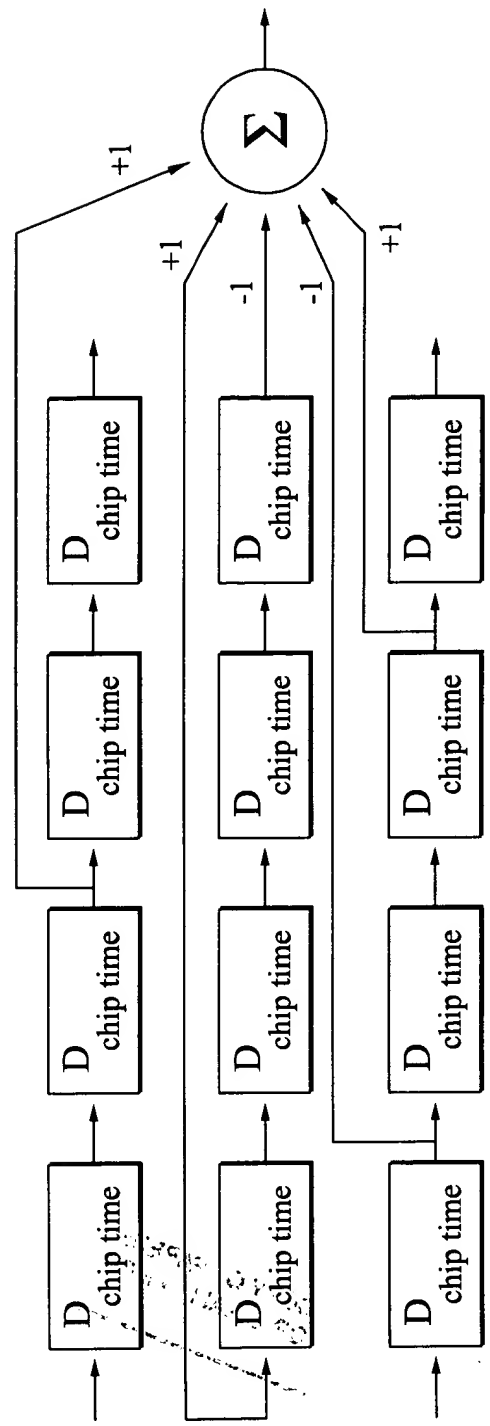Attorney: J.M. Breedlove
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5/5

Fig. 8

Outputs of pulse-pair
correlators for a range of lags